



Atty. Dkt. No.: TSL-105

2814

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Ishai Nachumovsky

Application No.: 09/975,049

Group No.: 2814

Filing Date: 10/10/2001

Examiner: Howard Weiss

For: "TWO-BIT SPLIT-GATE NON-VOLATILE MEMORY TRANSISTOR"

**BOX NON-FEE AMENDMENT**

Assistant Commissioner for Patents

Washington, D.C. 20231-0001

RECEIVED  
APR - 3 2003  
TO 2000 MAIL ROOM

Date: March 26, 2003

**AMENDMENT TRANSMITTAL**

1. Transmitted herewith is an amendment for this application.
2. **STATUS:** Applicant is other than a small entity.
3. **EXTENSION OF TERM:** The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply. Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.
4. **FEES FOR CLAIMS:** The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(Col.1)	(Col. 2)	(Col. 3)	LARGE ENTITY		
Claims					
Remaining	Highest No.				
After	Previously	Present			
Amendment	Paid For	Extra	Rate	Addit.	Fee
Total	19	Minus	20	= 0	x \$18 = \$0
Indep.	2	Minus	3	= 0	x \$84 = \$0
First Presentation of Multiple Dependent Claim			+ \$280 =	\$0	
			Total		
			Addit. Fee	\$0	

**No additional fee for claims is required.**

5. **FEES DEFICIENCY:** If any additional extension and/or fee is required, please charge Deposit Account No. 50-0574.

**SIGNATURE OF PRACTITIONER**

E. Eric Hoffman

Reg. No. 38,186

Customer No. 022888  
Tel.: (925) 895-3545

I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS MAIL in an envelope addressed to: Box Non-Fee Amendment, Assistant Commissioner for Patents, Washington, D.C., 20231, on 3/26/2003.

Date: 3/26/03 Signature: Carrie Redlick

Q #8/Amndt B  
4.8.03  
CMoore



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE CMoore

Inventor: Ishai Nachumovsky  
Assignee: Tower Semiconductor, Ltd.  
Title: TWO-BIT SPLIT-GATE NON-VOLATILE MEMORY TRANSISTOR  
Serial No.: 09/975,049 Group No.: 2814  
Filed: 10/10/2001 Examiner: Howard Weiss  
Docket No.: TSL-105

---

Date: March 26, 2003

Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231-0001

AMENDMENT IN RESPONSE TO SECOND OFFICE ACTION

Sir:

Applicant submits the following remarks in response to the Office Action dated January 3, 2003.

RECEIVED  
APR - 3 2003  
TC 283U MAIL ROOM